



K.S.R.M. COLLEGE OF ENGINEERING
(UGC-AUTONOMOUS)

Kadapa, Andhra Pradesh, India- 516 003

Approved by AICTE, New Delhi & Affiliated to JNTUA, Ananthapuramu.

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Lr./KSRMCE/ (EEE Committee/Cell name)/2021-22/

Date: 13.04.2022

To

The Principal,
K.S.R.M.College of Engineering
Kadapa.

Respected Sir,

//THROUGH PROPER CHANNEL//

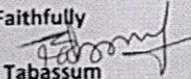
Sub:- Permission for Conducting a Three day Workshop on "Design of Electrical Circuits Using MULTISIM"(Phase -II) Requested - Submitted - Reg.

It is being brought to your kind notice that, the department of EEE in association with IEEE KSRMCE- SB is going to conduct a Three day Workshop on "Design of Electrical Circuits Using MULTISIM"(Phase-II) for IV Sem EEE students from 21/04/2022 to 23/04/2022.

In this regard I kindly request you to sanction the amount of Rs. 3000/- for smooth conduction of Workshop for which kind of act I would be grateful to you sir.

Thanking you

Yours Faithfully


Saleha Tabassum
Assistant Professor
Department of EEE

*Forwarded to
principal sir
kindly consider the
request & give permission
to conduct workshop.*

*Ugma
13/04/2022*

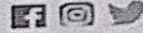
Permitted

*V. S. S. Muthy
18/04/2022*



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Department of Electrical & Electronics Engineering

Date: 18.04.2022

CIRCULAR

This is to inform that the department of Electrical & Electronics Engineering is organizing Workshop for B.Tech., IV Semester Students. In this regards, I request interested students to register their names with concern coordinator. The details of the Workshop is as follows,

Name of the Event	A Three day Workshop
Name of the workshop	Design of Electrical Circuits using MULTISIM (Phase-II)
Date(s) of the workshop	21.04.2020 to 23.04.2022
Timings of the workshop	09:00 AM to 04:00PM
Resource persons	Mr. K. Mohan Krishna, IV Semester Mr. V. Jagadeesh, IV Semester
Venue	SJ-114
Faculty Coordinator	Smt. Saleha Tabassum, Assistant Professor Mr. T.Naresh, Assistant Professor

V. Jayashankar
18/04/22
Head of the Department



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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

In Association with IEEE Student Branch



IEEE
KSRMCE SB

Organizing a

Three Day Workshop on

Design of Electrical Circuits Using MULTISIM(Phase-II)

Faculty Coordinators

Smt.Saleha Tabassum,Asst.Prof

Mr.T.Naresh,Asst.Prof

Resource Persons

A.Mohan Krishna - IV Sem ,EEE

V.Jagadeesh - IV Sem ,EEE

Dates: 21/04/2022 to 23/04/2022, Venue: SJ 114

Dr. K. Amaresh
(HOD)

Dr. V.S.S. Murthy
(Principal)

Dr. Kandula Chandra Obul Reddy
(MD, KGI)

Smt. K.Rajeswari
(Correspondent Secretary, Treasurer)

Sri K. Madan Mohan Reddy
(Vice - Chairman)

Sri K. Raja Mohan Reddy
(Chairman)



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Department of Electrical & Electronics Engineering

Three day workshop on "Design of Electrical Circuits using MULTISIM (Phase-II)"

Schedule

Timing: 09:00 AM – 04:00 PM

S.No	Date	Session	Resource Person	Topic Covered
1	21.04.2022	FN	Mrs. Saleha Tabassum Ms.M. Gowri	1.Applications of MULTISIM Simulation 2. Design of Monostable Circuits.
		AN	Ms.S. Alisha Sameera Ms. Neeraja Reddy	3. Design of Schmitt Trigger Circuit with various input Waveforms. 4. Design of Multivibrator Circuits.
2	22.04.2022	FN	Mr. V. Jagadeesh Mr. Sai Vignesh	1. Design of Traffic Light Control System. 2. Design of Stepper Motor Ring Distributer Circuit.
		AN	Mr. S. Md. Javeed Mr. M.C. Bharath Kumar Reddy	3. Design of Digital Clock Temperature sensor Circuit.
3	23.04.2022	FN	Mr. T. Naresh Mr. K. Mohan Krishna	1. Design of Noise Analysis Circuit. 2. Design of Virtual Moderator circuit using Fourier series Approximation.
		AN	Mr. A. Mohan Krishna Ms. S. Shashikala	3. Design of Random Signal Generator Circuit. 4. Design of Virtual Moderator circuit using QPSK Scheme.

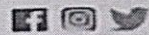
Coordinator(s)

Head of the Department



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Since 1980

Department of Electrical and Electronics Engineering

Department of Electrical & Electronics Engineering

List of Registered Participants

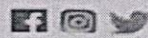
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Resource persons	Mr. K. Mohan Krishna IV Semester Mr. V. Jagadeesh IV Semester
Venue	SJ-114
Faculty Coordinator(s)	Smt. Saleha Tabassum, Assistant Professor Mr. T.Naresh, Assistant Professor

S.No	Name of the Student	Roll Number	Signature
1	A. Sanjana	209Y1A0203	Sanjana
2	B. Neeraja Reddy	209Y1A0204	B. Neeraja Reddy
3	C. Ramadevi	209Y1A0206	C. Ramadevi
4	D. bhawani	209Y1A0209	D. bhawani
5	D. Tejaswini	209Y1A0210	D. Tejaswini
6	G. Haxika	209Y1A0211	G. Haxika
7	G. Siva Kumar	209Y1A0214	G. Siva Kumar
8	K. Likhitha	209Y1A0217	K. Likhitha
9	K. Mohan Krishna	209Y1A0219	K. Mohan Krishna
10	M. Gauri	209Y1A0223	M. Gauri
11	M. Charan Bharath Kunoo Reddy	209Y1A0225	M. Charan Bharath
12	M. charan kumar	209Y1A0226	M. charan kumar



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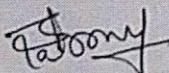
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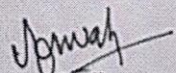
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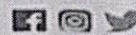
13	M. Mahalakshmi	20941A0229	M. Mahalakshmi
14	N. Sasi Rekha	20941A0229	N. Sasi Rekha
15	P. YOGNA	20941A0231	P. Yogna
16	P. Dayaranda	20941A0232	P. Dayaranda
17	P. Pavi Shankar	20941A0233	P. Pavi Shankar
18	P. Sai Vignesh	20941A0235	P. Sai Vignesh
19	R. Madhukrishna	20941A0236	R. Madhukrishna
20	R. Thirumala.	20941A0237	R. Thirumala
21	S. S Reekanth.	20941A0238	
22	S. Ramu.	20941A0239	
23	S. Alisha Sameera	20941A0241	
24	S. Mohammad Javeed	20941A0243	S. Javeed
25	S. Sudharshan	20941A0246	S. Sudharshan
26	S. Shashikala	20941A0247	S. Shashikala
27	T. Pallavi	20941A0249	T. Pallavi
28	V. Vinay	20941A0251	V. Vinay
29	V. Jagadeesh	20941A0252	V. Jagadeesh
30	A. Nagarajua	21945A0201	A. Nagarajua
31	B. Chandra Kumar	21945A0205	B. Chandra Kumar
32	M. Sainidhi	21945A0204	M. Sainidhi
33	M. Lakshmi devi	21945A0205	M. Lakshmi devi
34	N. Prudvi Raj	21945A0206	N. Prudvi Raj


Signature of Coordinator(s)


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List of Participant

Name of the Event	A Three day Workshop
Name of the workshop	Design of Electrical Circuits using MULTISIM (Phase-II)
Date(s) of the workshop	21.04.2020 to 23.04.2022
Timings of the workshop	09:00 AM to 04:00PM
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Venue	SJ-114
Faculty Coordinator	Smt. Saleha Tabassum Assistant Professor Mr. T.Naresh Assistant Professor

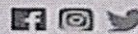
Student Attendance

S.No	Roll Number	21-04-22 (FN)	21-04-22 (AN)	22-04-22 (FN)	22-04-22 (AN)	23-04-22 (FN)	23-04-22 (AN)
1	20941A0203	P	P	P	P	P	P
2	20941A0204	P	P	P	P	P	P
3	20941A0206	P	P	P	P	P	P
4	20941A0209	P	P	P	P	P	P
5	20941A0210	P	P	P	P	P	P
6	20941A0211	P	P	P	P	P	P
7	20941A0214	P	P	P	P	P	P
8	20941A0217	P	P	P	P	P	P
9	20941A0219	P	P	P	P	P	P
10	20941A0223	P	P	P	P	P	P
11	20941A0225	P	P	P	P	P	P
12	20941A0226	P	P	P	P	P	P
13	20941A0227	P	P	P	P	P	P



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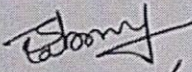
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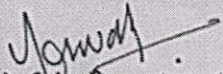
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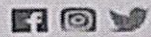
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15	20941A0231	P	P	P	P	P	P
16	20941A0232	P	P	P	P	P	P
17	20941A0233	P	P	P	P	P	P
18	20941A0235	P	P	P	P	P	P
19	20941A0236	A	A	A	P	P	P
20	20941A0237	P	P	P	P	P	P
21	20941A0238	P	P	P	P	P	P
22	20941A0239	P	P	P	P	P	P
23	20941A0241	P	P	P	P	P	P
24	20941A0243	P	P	P	P	P	P
25	20941A0246	P	P	P	P	A	P
26	20941A0247	P	P	P	P	P	P
27	20941A0249	P	P	P	P	P	P
28	20941A0251	P	P	P	P	P	P
29	20941A0252	P	P	P	P	P	P
30	21945A0201	P	P	P	P	P	P
31	21945A0203	P	P	P	P	P	P
32	21945A0204	P	P	P	P	P	P
33	21945A0205	P	P	P	P	P	P
34	21945A0206	P	P	P	P	P	P


Signature of Coordinator(s)


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Department of Electrical & Electronics Engineering
Activity Report

Name of the Event	Three day Workshop
Name of the workshop	Design of Electrical Circuits Using MULTISIM (Phase-II)
Date(s) of the workshop	21-04-2022 to 23-04-2022
Target Audience	B.Tech., IV Semester Students
Number of Students Participated	32
Resource Persons (B. Tech., IV Semester EEE Students)	K. Mohan Krishna, V.Jagadeesh, C. Bharath Kumar Reddy, S. Md. Javeed, P. Sai Vignesh, B.Neeraja Reddy, M. Gouri, S. Alisha Sameera, S Shashikala
Organizer/Supporting Team	Smt. Saleha Tabassum, Assistant Professor Mr. T. Naresh, Assistant Professor
Report	MULTISIM is one of the important tools for electrical students to learn the circuit designing. Department of EEE organized a Workshop on "Design of Electrical Circuits Using MULTISIM (Phase-II)". Head of the Department, faculty & participants of the workshop inaugurated the workshop with all good spirit. Resource persons began with the Various Applications of MULTISIM Software and designing of Multivibrators, Schmitt Triggering circuits, Stepper motor ring distributors, Traffic light control systems, 24 bit counter designing and random signal generation. Students also learned about how to write the journal paper and PCB design for the executed circuits. Students have designed various projects and done hands on practiced circuits using MULTISIM.



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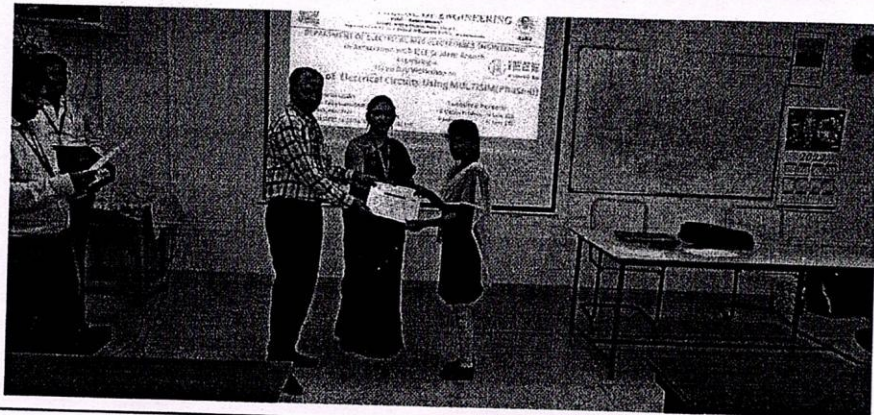
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Photos



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Department of Electrical and Electronics Engineering

Three day workshop on "Design of Electrical Circuits using MULTISIM (Phase-II)"

Student Feed Back Form

Date: 23/04/22

Name of the Student: N. SasiRexha

Roll Number: 20941A0229

Mail ID: 20941A0229@gmail.com
20941A0229@kasmce.ac.in

1. How would you rate the usefulness of content

1 2 3 4 5 ✓

2. How would you rate the hands-on Activity

1 2 3 4 ✓ 5

3. How would you rate the presentation of the Activity

1 2 3 4 5 ✓

4. How would you rate the presentation style of teaching


1 2 3 4 ✓ 5

5. Was the workshop Above or Below your current skill level

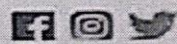
Above Below Just right ✓

6. Any other Suggestions : NO

N. SasiRexha
Signature of the Student

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CERTIFICATE OF PARTICIPATION



IEEE
KSRMCE SB

This is to certified that *Mr/Ms* . _____
bearing Roll No _____ has participated in a three day
workshop on “ Design of Electrical Circuits using MULTISIM
(Phase-II) ” organized by Department of EEE in Association with IEEE
Student Branch from 21 to 23 April, 2022.

Saleha Tabassum
Coordinator

Dr.K.Amaresh
Prof & HOD

Prof.V.S.S Murthy
Principal



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Department of Electrical & Electronics Engineering

Date: 23.04.2022

Feedback

Name of the student: *S.Md. Javeed*

Roll Number: *209YMA0263*

Email ID:

- How would you rate the usefulness of the content
1. Strongly Agree 2. Agree 3. Neutral 4. Disagree 5. Strongly Disagree
- Interaction of the faculty with the students
1. Strongly Agree 2. Agree 3. Neutral 4. Disagree 5. Strongly Disagree
- How would you rate the presentation of the activity
1. Strongly Agree 2. Agree 3. Neutral 4. Disagree 5. Strongly Disagree
- How would you rate the presentation style of teaching
1. Strongly Agree 2. Agree 3. Neutral 4. Disagree 5. Strongly Disagree
- Was the workshop Above or Below your current skill level
 Above Below Just right
- Any other suggestions

S.Md. Javeed
Signature of the student



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CERTIFICATE OF PARTICIPATION



IEEE
KSRMCE SB

This is to certified that *Mr/Ms . S. MD. Javeed*
bearing Roll No 209Y1A0243 has participated in a three day
workshop on “ Design of Electrical Circuits using MULTISIM
(Phase-II) ” organized by Department of EEE in Association with IEEE
Student Branch from 21 to 23 April, 2022.

Saleha Tabassum
Coordinator

Dr. K. Amaresh
Prof & HOD

Prof. V. S. S. Murthy
Principal